

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: **TA-CHIN LIN, YI-FENG HUANG, FU-KANG LAI, JEN-CHIH HSIAO**

For: **ALGORITHMS TUNNING FOR DYNAMIC LOT DISPATCHING IN WAFER AND CHIP PROBING**

03970 U.S. PTO  
10/672403



Enclosed are:

**11** sheets of drawing(s) - formal.

An assignment of the invention to **Taiwan Semiconductor Manufacturing Co.**

An associate power of attorney  Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
<b>BASIC FEE</b>				<b>\$ 750.</b>
<b>TOTAL CLAIMS</b>	<b>32 -20=</b>	<b>12</b>	<b>x 18 =</b>	<b>\$ 216.</b>
<b>INDEP CLAIMS</b>	<b>2 -3=</b>	<b>0</b>	<b>x 84 =</b>	<b>\$ 0.</b>
		<b>SUB TOTAL</b>		<b>\$ 966.</b>
		<b>ASSIGNMENT</b>		<b>\$40.</b>
		<b>TOTAL</b>		<b>\$ 1,006.</b>

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Any additional filing fees required under 37 CFR §1.16.

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Respectfully submitted,

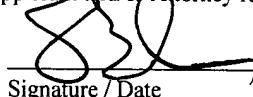
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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

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